

WHAT IS CLAIMED IS:

1. A video signal processing apparatus,
comprising:
 - an amplifier having an input for receiving a
5 chroma input signal, an output for providing a chroma
output signal;
 - a control circuit coupled to the amplifier for
controlling the amplitude of a burst component of the
output signal characterized in that
- 10 the control circuit reduces the amplitude of
the output signal in a controlled manner when the value
of the burst component is below a predetermined value.
2. Apparatus according to Claim 1,
15 characterized in that the control circuit comprises a
first feedback path coupled to the amplifier and a
second feedback path coupled to the first feedback path.
3. Apparatus according to Claim 2,
20 characterized in that the first feedback path comprises
a filter and the second feedback path is connected in
parallel with the filter for controlling the values of a
characteristic knee and/or slope characteristic of the
apparatus for burst amplitudes below a given knee value.
- 25 4. In a television apparatus, an ACC system,
comprising:
 - a controllable amplifier having an input for
receiving a chroma input signal, an output for providing
30 an chroma output signal of controllable amplitude, and a
gain control input; and
 - a circuit for controlling the gain of the
controllable amplifier, characterized in that the
circuit comprises
- 35 a first feedback path including a cascade
connection of a means for providing a signal
representative of a measured burst amplitude, a means
for providing an error signal representative of the
difference between a desired burst amplitude and the

measured burst amplitude, and an integrator, coupled between the output of the controllable amplifier and a gain control input of the controllable amplifier, for integrating the error signal, and

- 5 a second feedback path coupled from an output of the integrator to an input of the integrator for reducing the gain of the amplifier at a predetermined rate for values of a burst component of the chroma input signal below a predetermined threshold value.

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5. The ACC system according to Claim 4, characterized in that the second feedback path includes:
circuit means for scaling, offsetting and limiting the integrator output signal to provide a
15 control signal; and
means for applying the control signal to the input of the integrator.

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6. The ACC system according to Claim 4,
characterized in that the means for providing a signal representative of a measured burst amplitude comprises a burst gate.

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7. The ACC system according to Claim 4,
characterized in that the means for providing a signal representative of a measured burst amplitude comprises a chroma demodulator and a burst accumulator.